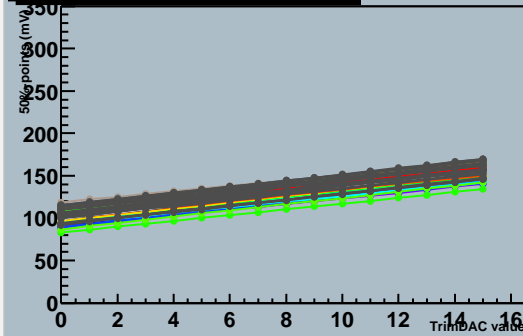


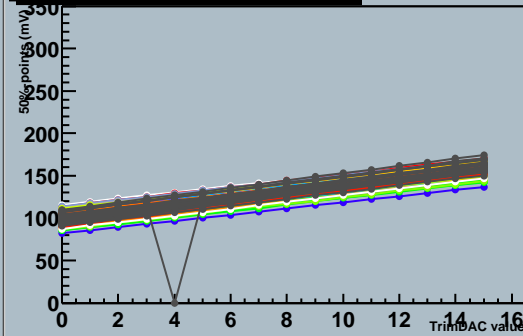
ATLAS SCT Module Test - Fri Oct 20 02:07:38 2000
Unknown - Module 0170200003b220s30q2t140r0
Run 1743 Trim Scans Module 0 Link 0 TrimR 0

Target value to be 125 mV
This gives 1533 trimmable channels

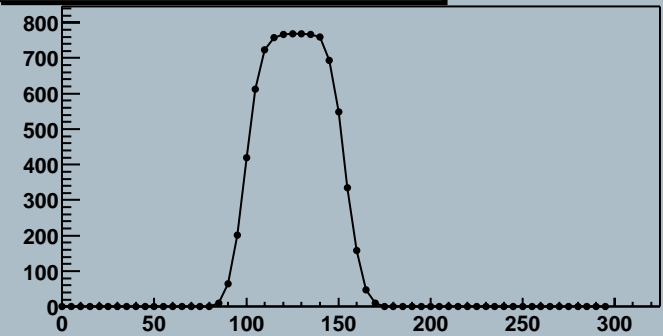
TrimDAC characteristics, chip0



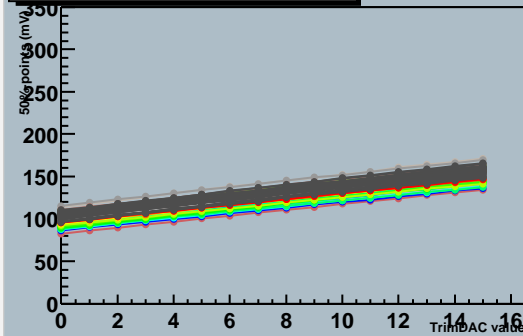
TrimDAC characteristics, chip1



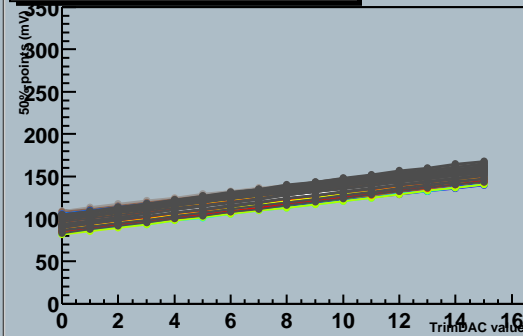
Trimmable channels, chip 0 to 5



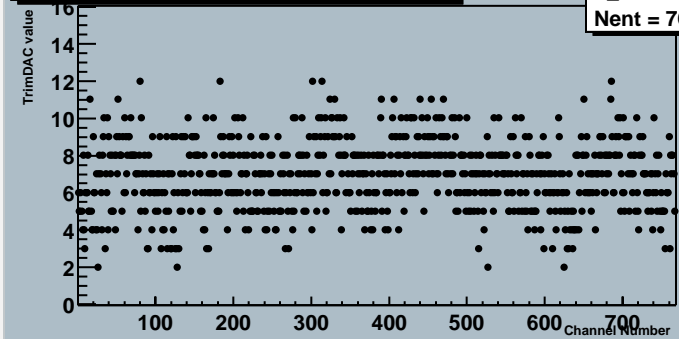
TrimDAC characteristics, chip2



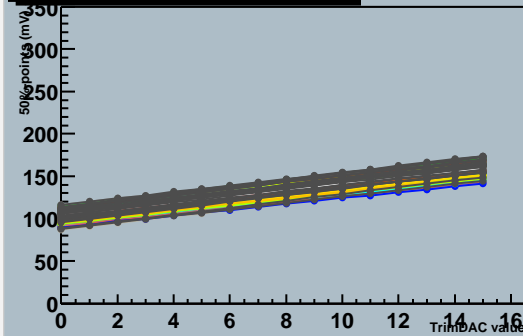
TrimDAC characteristics, chip3



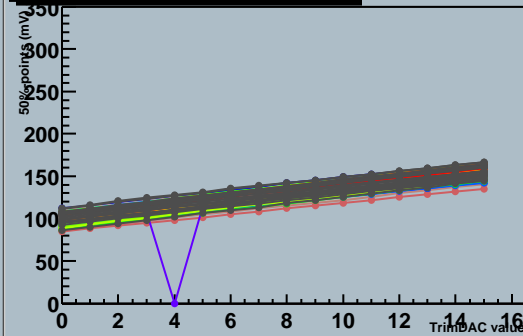
Trim Setting - Trimmable Channels



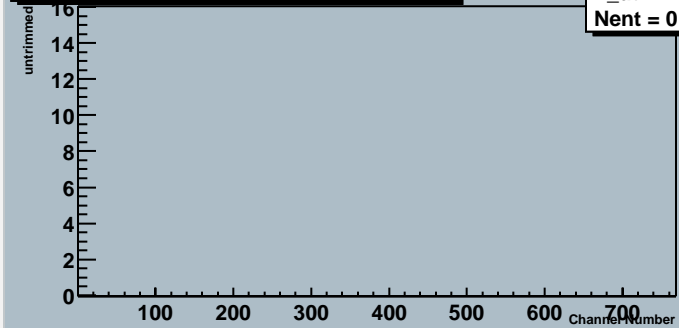
TrimDAC characteristics, chip4



TrimDAC characteristics, chip5



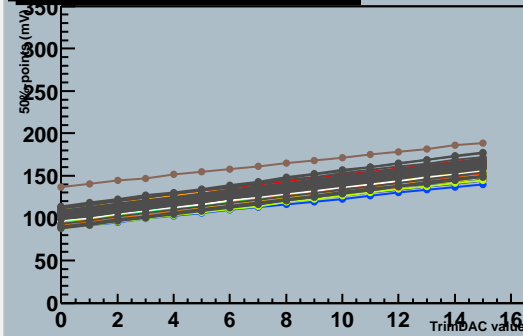
Trim Setting - Untrimmable Channels



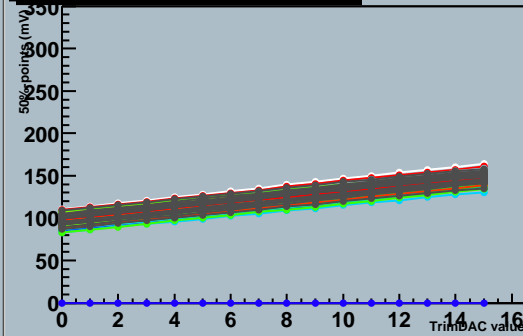
ATLAS SCT Module Test - Fri Oct 20 02:07:38 2000
 Unknown - Module 0170200003b220s30q2t140r0
 Run 1743 Trim Scans Module 0 Link 1 TrimR 0

Target value to be 125 mV
 This gives 1533 trimmable channels

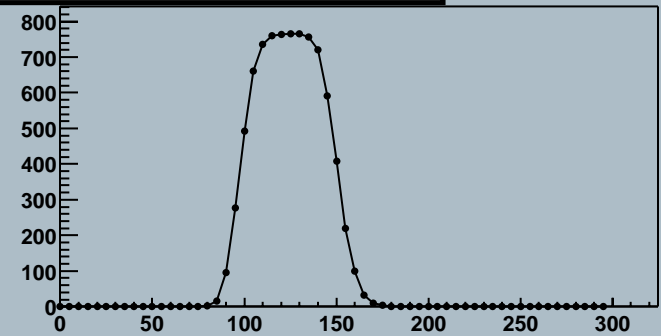
TrimDAC characteristics, chip 6



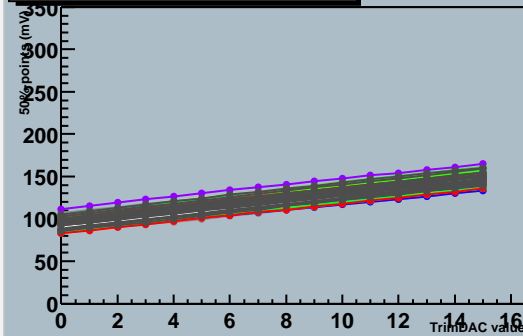
TrimDAC characteristics, chip 7



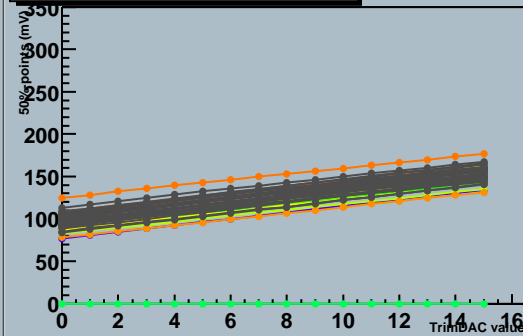
Trimmable channels, chip 6 to 11



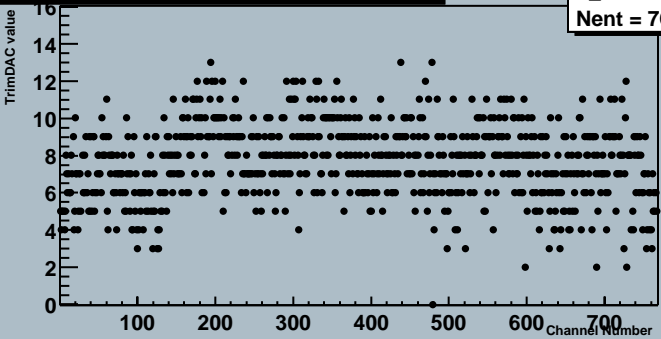
TrimDAC characteristics, chip 8



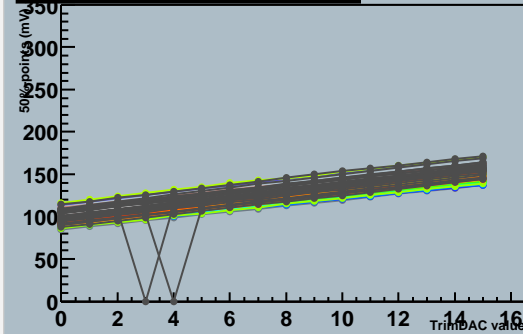
TrimDAC characteristics, chip 9



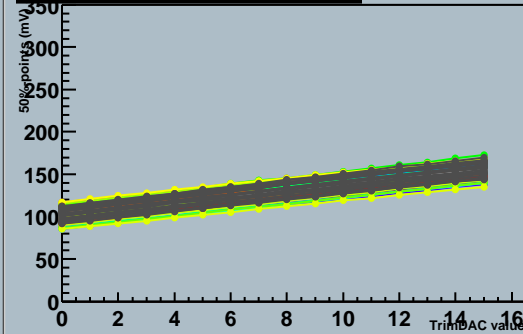
Trim Setting - Trimmable Channels



TrimDAC characteristics, chip 10



TrimDAC characteristics, chip 11



Trim Setting - Untrimmable Channels

