

Chip Z36459A-W03-X07-Y19 report.
(created: Thu Mar 29 20:07:19 2001)

General info :

Wafer name : Z36459A-W03
Position in wafer: X07-Y19
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 54.00 [mA] 80.00 [mA]
Slave digital and analog supply current : 36.00 [mA] 80.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 58.57 / 1.53 [mV/fC]
Chip offset / sigma : 16.02 / 7.29 [mV]
Chip output noise (max): 12.25 [mV]
Chip output noise (min): 5.14 [mV]
Quality factor: 7.86

DAC data:

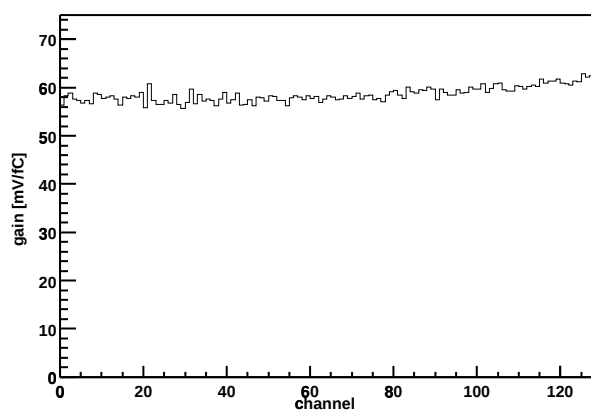
Threshold DAC slope:	2.51 [mV/bit]	lin. error:	0.4 [%]
Preamp. DAC slope:	2.83 [mV/bit]	lin. error:	3.8 [%]
Shaper DAC slope:	1.72 [mV/bit]	lin. error:	5.6 [%]

Range DAC data:

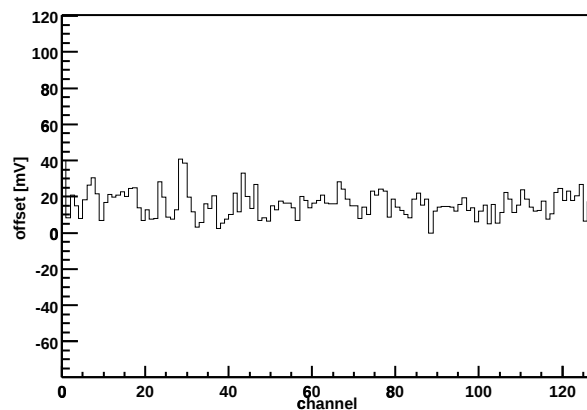
range 0 :	58.2 [mV]
range 1 :	113.3 [mV]
range 2 :	166.3 [mV]
range 3 :	217.1 [mV]

Other data is available on plots following this page.

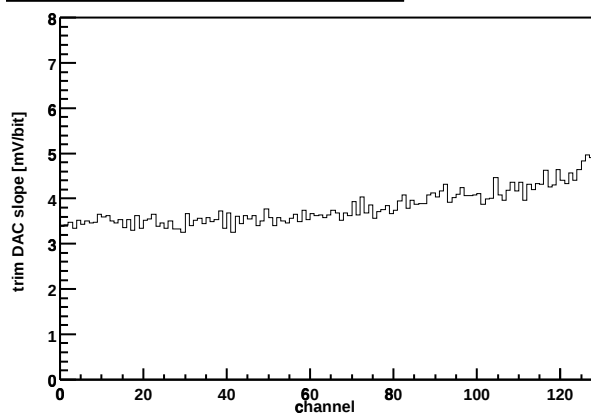
Chip Z36459A-W03-X07-Y19 Gain vs. channel



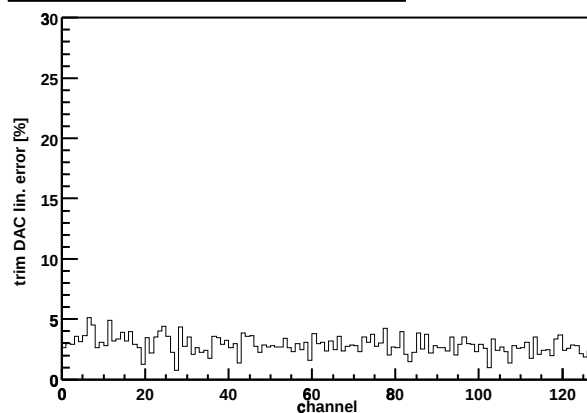
Chip Z36459A-W03-X07-Y19 Offset vs. channel



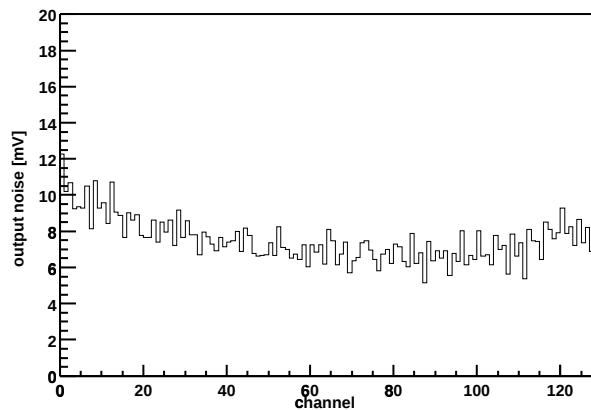
Chip Z36459A-W03-X07-Y19 Trim DAC slope vs. channel



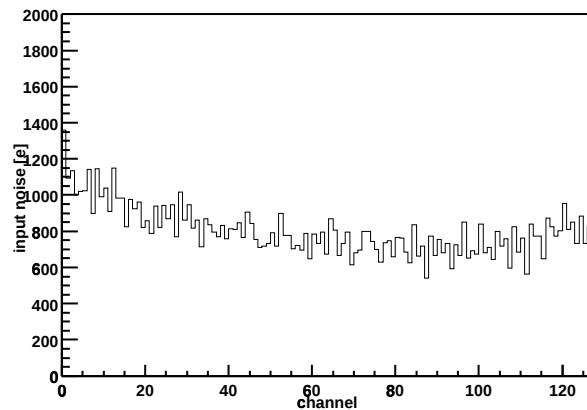
Chip Z36459A-W03-X07-Y19 trim DAC lin. error



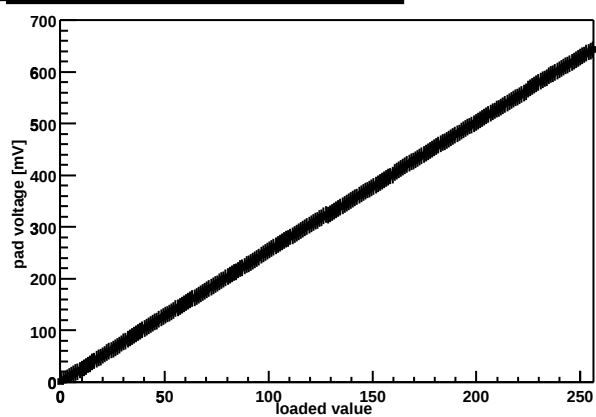
Chip Z36459A-W03-X07-Y19 Output noise vs. channel



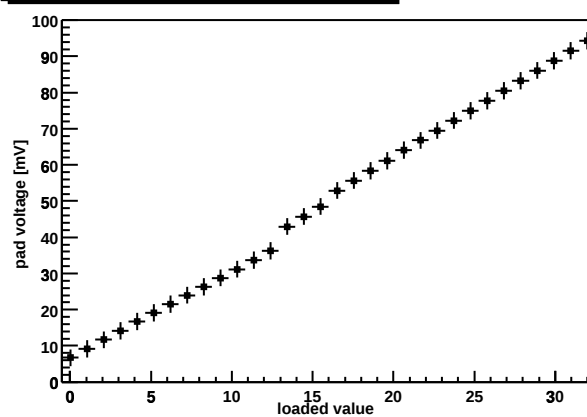
Chip Z36459A-W03-X07-Y19 Input noise vs. channel



Chip Z36459A-W03-X07-Y19 threshold DAC



Chip Z36459A-W03-X07-Y19 preamp DAC



Chip Z36459A-W03-X07-Y19 shaper DAC

