

Chip Z36459A-W03-X10-Y05 report.
(created: Thu Mar 29 20:06:50 2001)

General info :

Wafer name : Z36459A-W03
Position in wafer: X10-Y05
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 53.00 [mA] 80.00 [mA]
Slave digital and analog supply current : 36.00 [mA] 80.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 55.25 / 1.77 [mV/fC]
Chip offset / sigma : 17.18 / 4.64 [mV]
Chip output noise (max): 11.94 [mV]
Chip output noise (min): 5.76 [mV]
Quality factor: 11.12

DAC data:

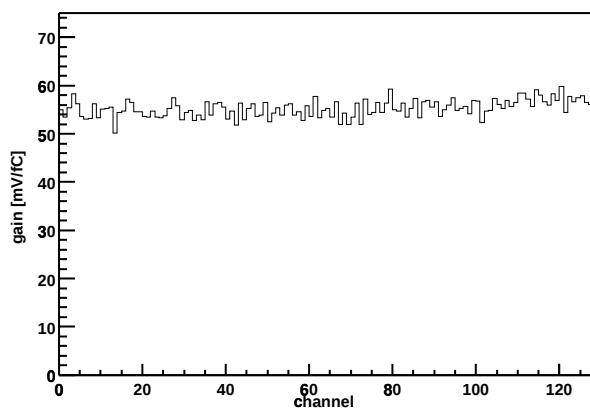
Threshold DAC slope:	2.51 [mV/bit]	lin. error:	0.4 [%]
Preamp. DAC slope:	2.70 [mV/bit]	lin. error:	2.9 [%]
Shaper DAC slope:	1.63 [mV/bit]	lin. error:	4.8 [%]

Range DAC data:

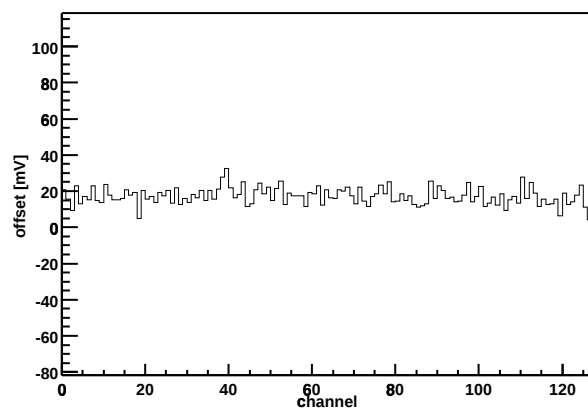
range 0 :	52.6 [mV]
range 1 :	106.2 [mV]
range 2 :	161.9 [mV]
range 3 :	210.7 [mV]

Other data is available on plots following this page.

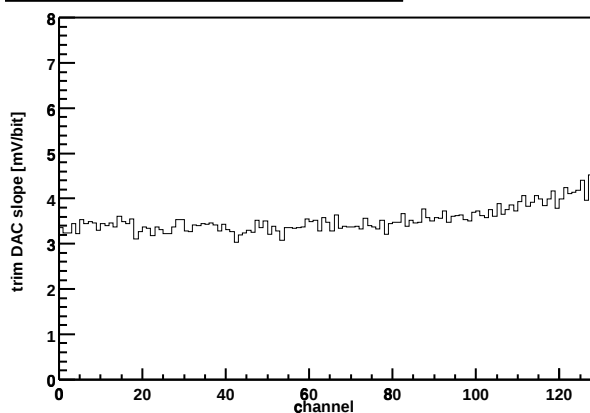
Chip Z36459A-W03-X10-Y05 Gain vs. channel



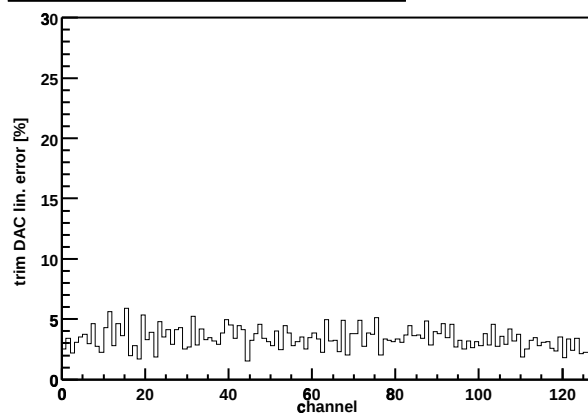
Chip Z36459A-W03-X10-Y05 Offset vs. channel



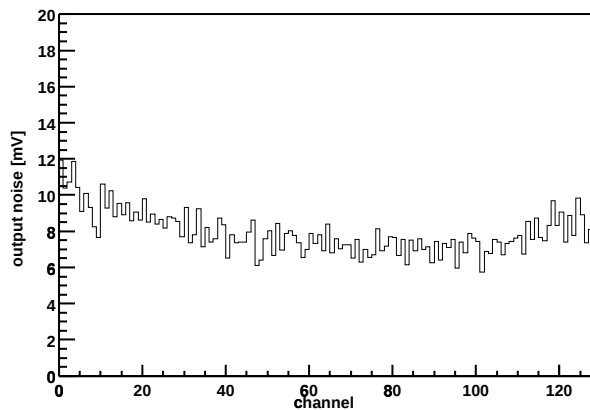
Chip Z36459A-W03-X10-Y05 Trim DAC slope vs. channel



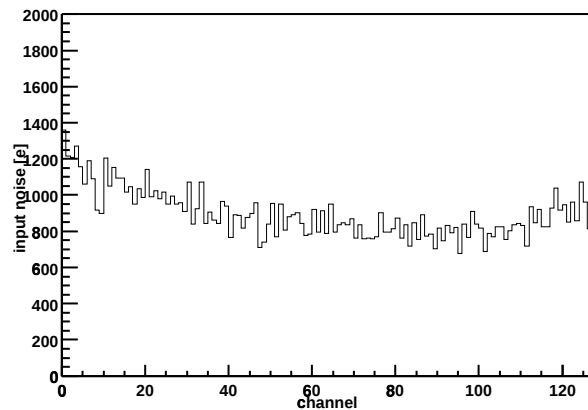
Chip Z36459A-W03-X10-Y05 trim DAC lin. error



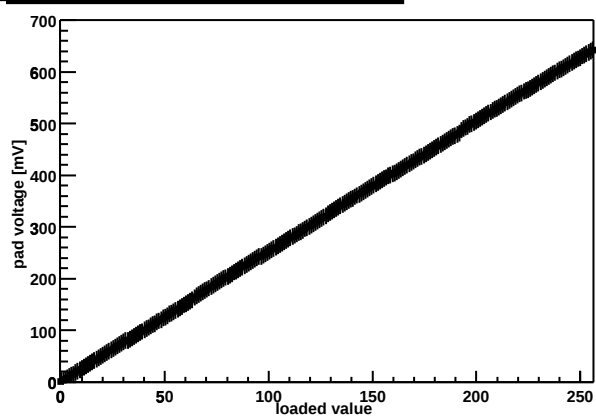
Chip Z36459A-W03-X10-Y05 Output noise vs. channel



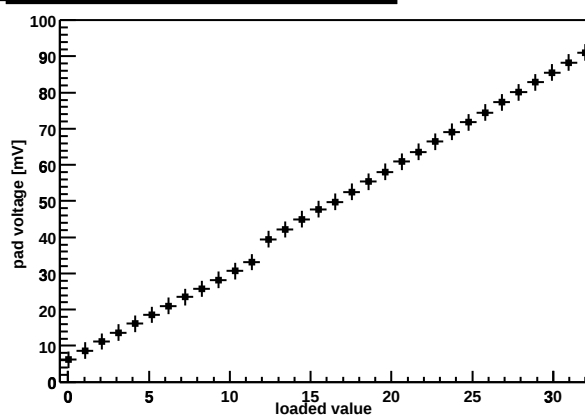
Chip Z36459A-W03-X10-Y05 Input noise vs. channel



Chip Z36459A-W03-X10-Y05 threshold DAC



Chip Z36459A-W03-X10-Y05 preamp DAC



Chip Z36459A-W03-X10-Y05 shaper DAC

