

Chip Z36459A-W03-X10-Y12 report.
(created: Thu Mar 29 20:07:09 2001)

General info :

Wafer name : Z36459A-W03
Position in wafer: X10-Y12
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 50.00 [mA] 80.00 [mA]
Slave digital and analog supply current : 35.00 [mA] 80.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 57.18 / 1.24 [mV/fC]
Chip offset / sigma : 12.70 / 6.41 [mV]
Chip output noise (max): 13.76 [mV]
Chip output noise (min): 6.44 [mV]
Quality factor: 8.76

DAC data:

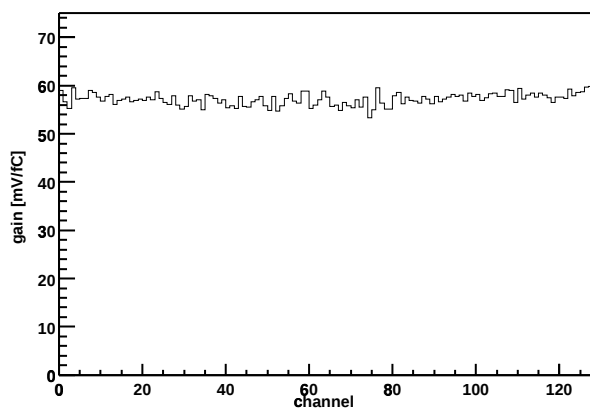
Threshold DAC slope:	2.55 [mV/bit]	lin. error:	0.4 [%]
Preamp. DAC slope:	2.88 [mV/bit]	lin. error:	3.8 [%]
Shaper DAC slope:	1.82 [mV/bit]	lin. error:	5.9 [%]

Range DAC data:

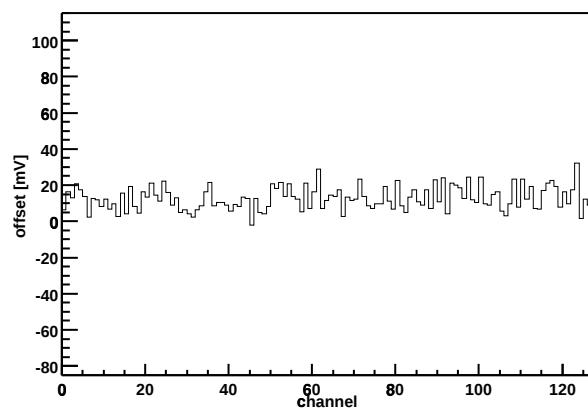
range 0 :	50.4 [mV]
range 1 :	102.4 [mV]
range 2 :	152.2 [mV]
range 3 :	201.0 [mV]

Other data is available on plots following this page.

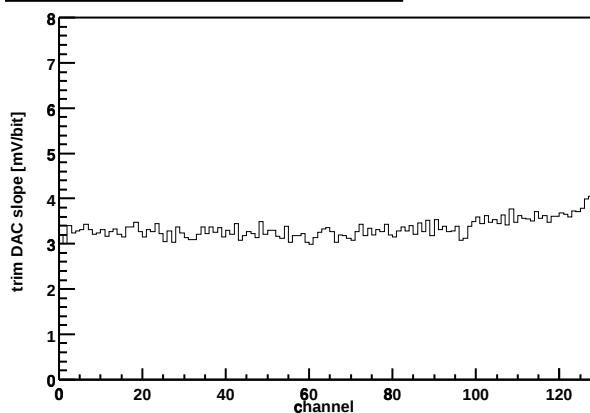
Chip Z36459A-W03-X10-Y12 Gain vs. channel



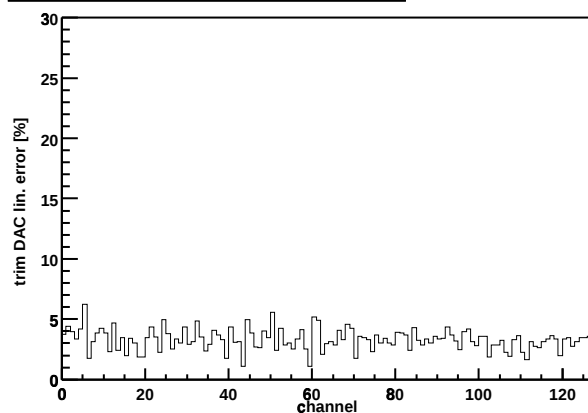
Chip Z36459A-W03-X10-Y12 Offset vs. channel



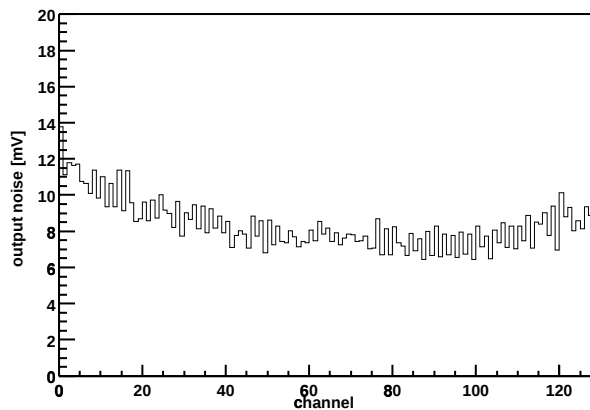
Chip Z36459A-W03-X10-Y12 Trim DAC slope vs. channel



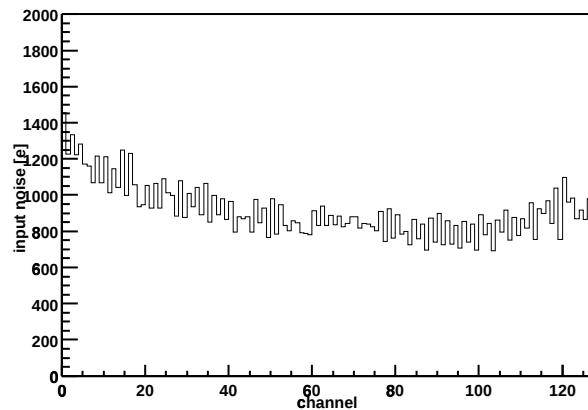
Chip Z36459A-W03-X10-Y12 trim DAC lin. error



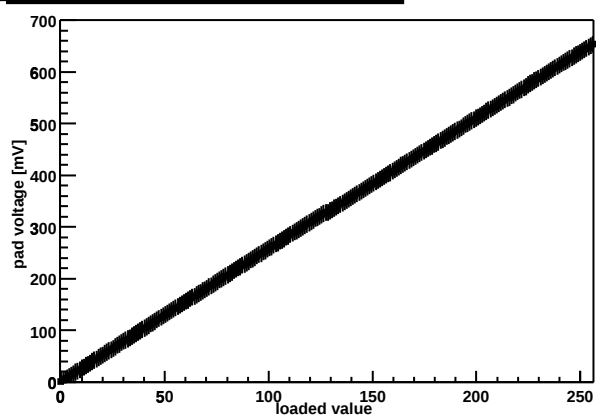
Chip Z36459A-W03-X10-Y12 Output noise vs. channel



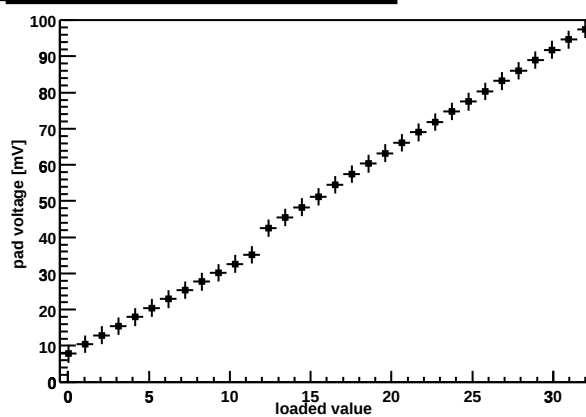
Chip Z36459A-W03-X10-Y12 Input noise vs. channel



Chip Z36459A-W03-X10-Y12 threshold DAC



Chip Z36459A-W03-X10-Y12 preamp DAC



Chip Z36459A-W03-X10-Y12 shaper DAC

