

Chip Z36459A-W03-X13-Y02 report.
(created: Thu Mar 29 20:06:43 2001)

General info :

Wafer name : Z36459A-W03
Position in wafer: X13-Y02
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 57.00 [mA] 76.00 [mA]
Slave digital and analog supply current : 36.00 [mA] 76.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.6
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 53.44 / 1.49 [mV/fC]
Chip offset / sigma : 14.08 / 7.85 [mV]
Chip output noise (max): 13.25 [mV]
Chip output noise (min): 5.55 [mV]
Quality factor: 6.68

DAC data:

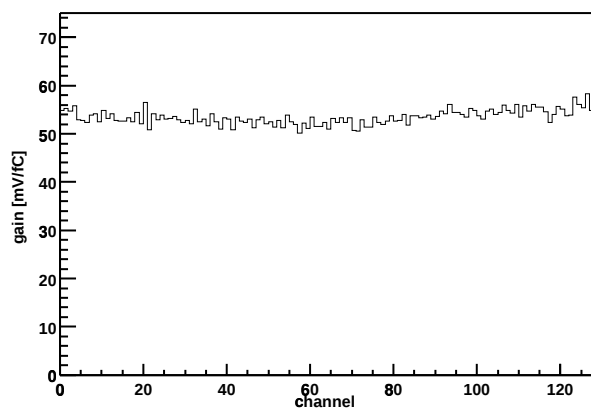
Threshold DAC slope:	2.55 [mV/bit]	lin. error:	0.3 [%]
Preamp. DAC slope:	2.80 [mV/bit]	lin. error:	3.1 [%]
Shaper DAC slope:	1.69 [mV/bit]	lin. error:	4.6 [%]

Range DAC data:

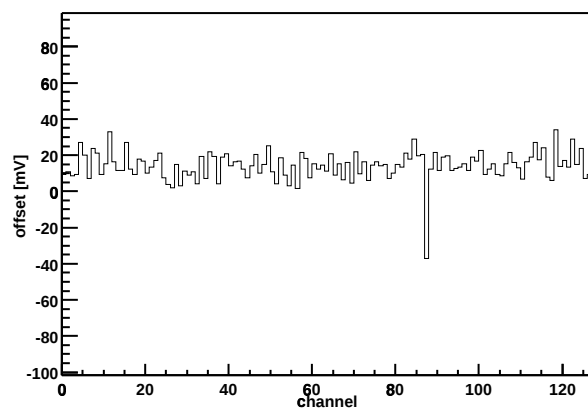
range 0 :	52.3 [mV]
range 1 :	104.3 [mV]
range 2 :	156.1 [mV]
range 3 :	206.1 [mV]

Other data is available on plots following this page.

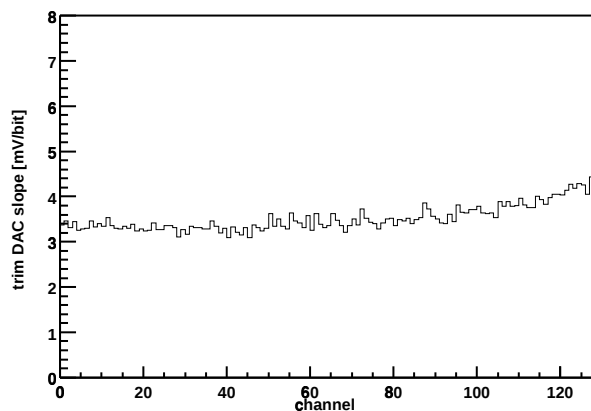
Chip Z36459A-W03-X13-Y02 Gain vs. channel



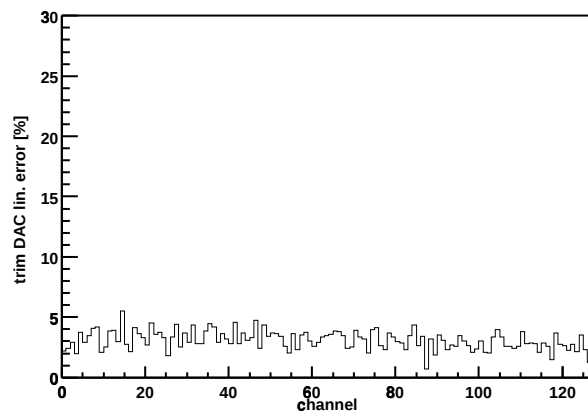
Chip Z36459A-W03-X13-Y02 Offset vs. channel



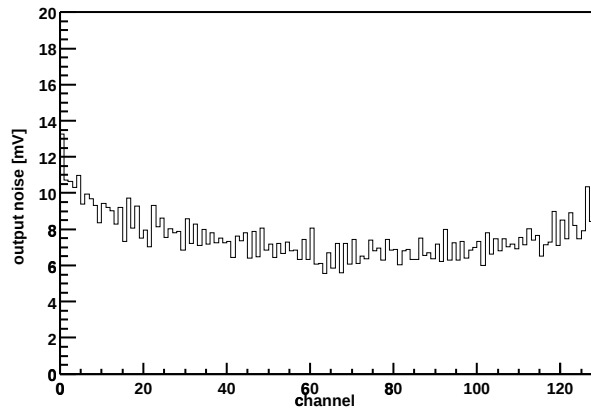
Chip Z36459A-W03-X13-Y02 Trim DAC slope vs. channel



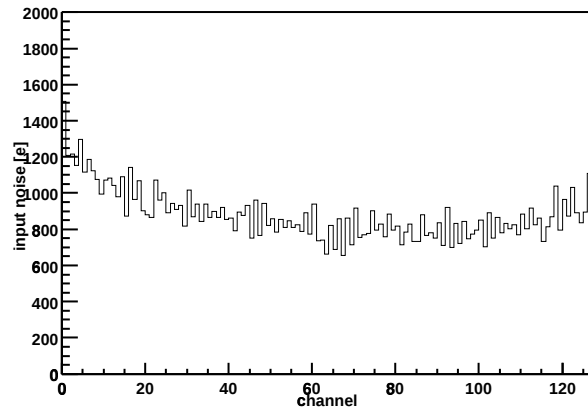
Chip Z36459A-W03-X13-Y02 trim DAC lin. error



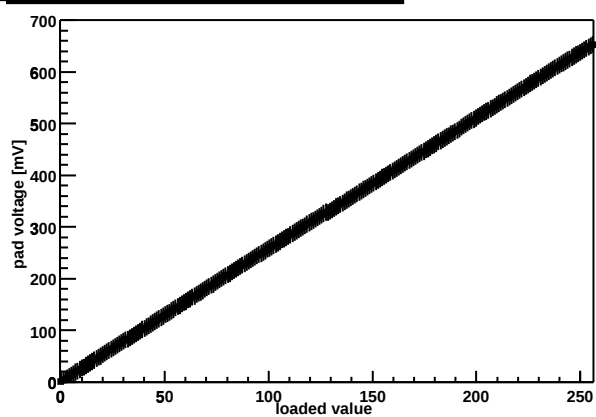
Chip Z36459A-W03-X13-Y02 Output noise vs. channel



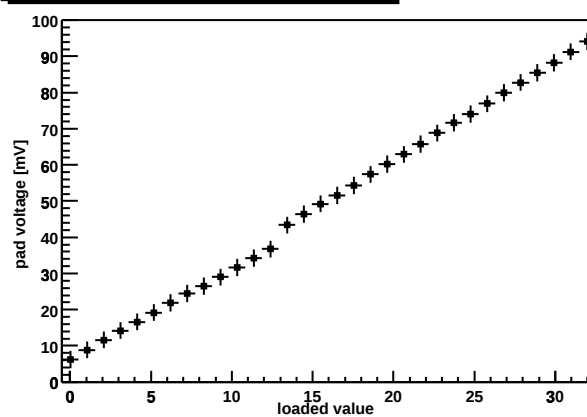
Chip Z36459A-W03-X13-Y02 Input noise vs. channel



Chip Z36459A-W03-X13-Y02 threshold DAC



Chip Z36459A-W03-X13-Y02 preamp DAC



Chip Z36459A-W03-X13-Y02 shaper DAC

