

Chip Z36459A-W04-X05-Y18 report.
(created: Thu Mar 29 20:54:41 2001)

General info :

Wafer name : Z36459A-W04
Position in wafer: X05-Y18
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 56.00 [mA] 84.00 [mA]
Slave digital and analog supply current : 36.00 [mA] 84.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.6	3.4
Token test:	3.3	3.3

Digital perfect pattern: no

Analog data:

Chip gain / sigma : 57.72 / 1.18 [mV/fC]
Chip offset / sigma : 15.14 / 8.37 [mV]
Chip output noise (max): 11.25 [mV]
Chip output noise (min): 5.23 [mV]
Quality factor: 6.83

DAC data:

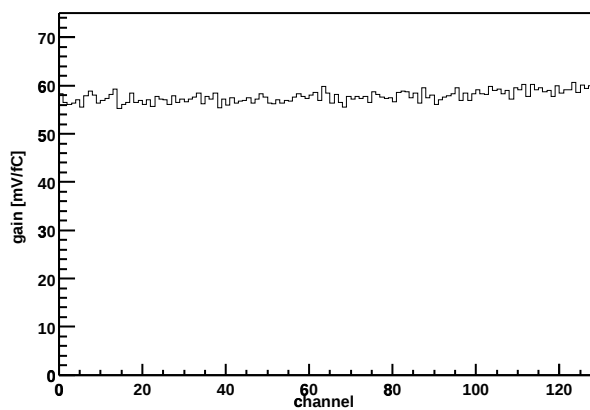
Threshold DAC slope:	2.55 [mV/bit]	lin. error:	0.4 [%]
Preamp. DAC slope:	2.95 [mV/bit]	lin. error:	3.8 [%]
Shaper DAC slope:	1.87 [mV/bit]	lin. error:	5.6 [%]

Range DAC data:

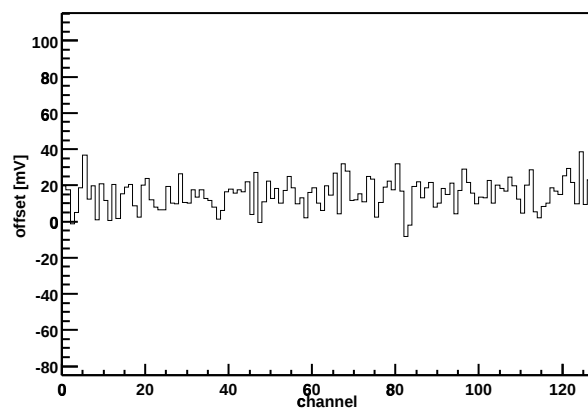
range 0 :	62.5 [mV]
range 1 :	118.5 [mV]
range 2 :	170.2 [mV]
range 3 :	223.0 [mV]

Other data is available on plots following this page.

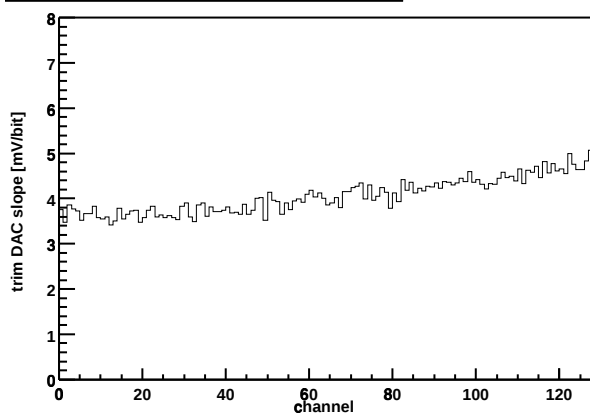
Chip Z36459A-W04-X05-Y18 Gain vs. channel



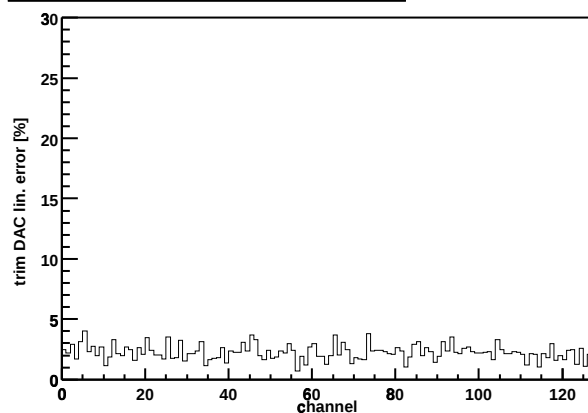
Chip Z36459A-W04-X05-Y18 Offset vs. channel



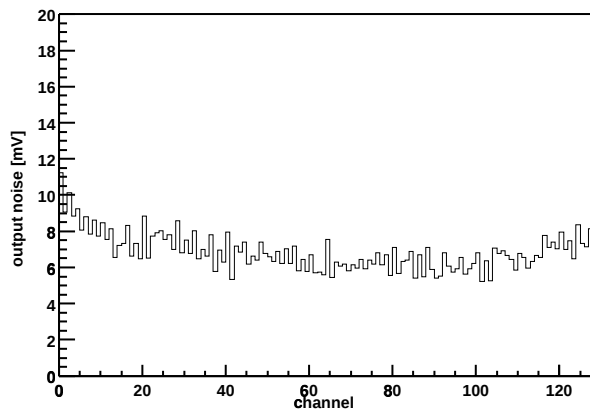
Chip Z36459A-W04-X05-Y18 Trim DAC slope vs. channel



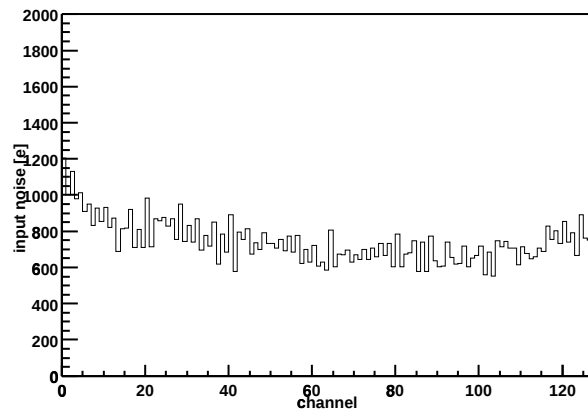
Chip Z36459A-W04-X05-Y18 trim DAC lin. error



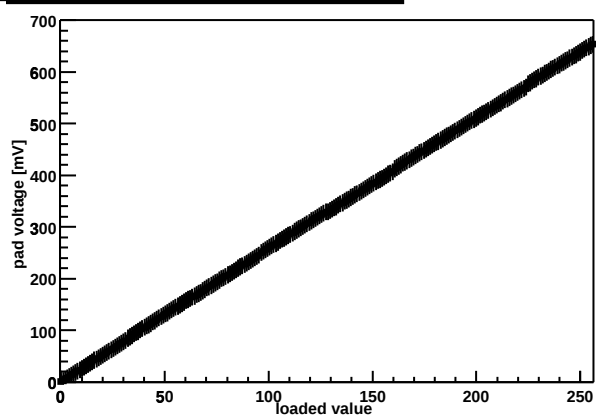
Chip Z36459A-W04-X05-Y18 Output noise vs. channel



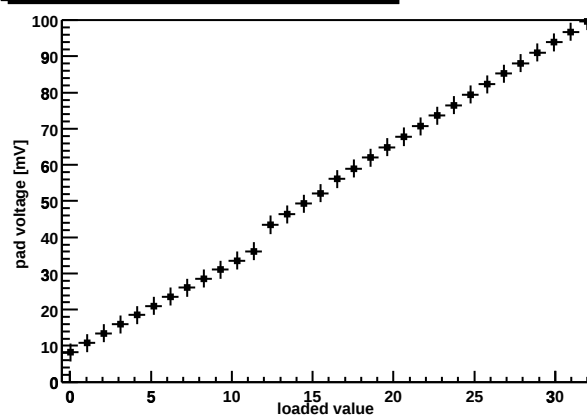
Chip Z36459A-W04-X05-Y18 Input noise vs. channel



Chip Z36459A-W04-X05-Y18 threshold DAC



Chip Z36459A-W04-X05-Y18 preamp DAC



Chip Z36459A-W04-X05-Y18 shaper DAC

