

Chip Z36459A-W04-X10-Y18 report.
(created: Thu Mar 29 20:54:44 2001)

General info :

Wafer name : Z36459A-W04
Position in wafer: X10-Y18
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 54.00 [mA] 82.00 [mA]
Slave digital and analog supply current : 35.00 [mA] 82.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 58.88 / 1.26 [mV/fC]
Chip offset / sigma : 14.36 / 6.87 [mV]
Chip output noise (max): 13.14 [mV]
Chip output noise (min): 5.29 [mV]
Quality factor: 8.43

DAC data:

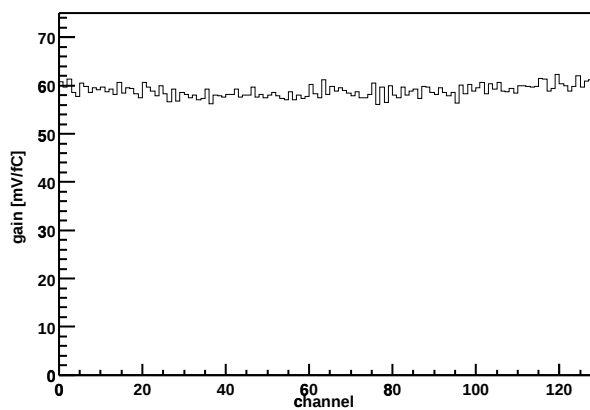
Threshold DAC slope:	2.49 [mV/bit]	lin. error:	0.3 [%]
Preamp. DAC slope:	3.01 [mV/bit]	lin. error:	4.9 [%]
Shaper DAC slope:	1.92 [mV/bit]	lin. error:	7.3 [%]

Range DAC data:

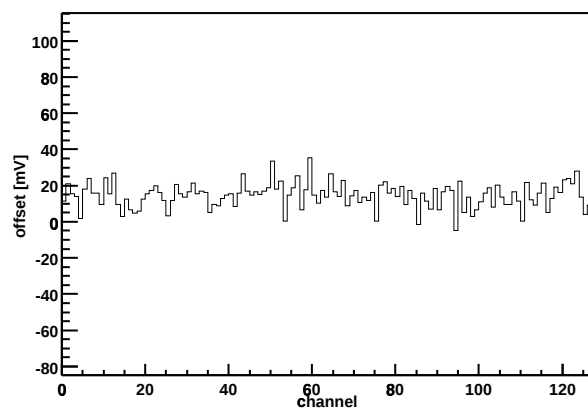
range 0 :	49.1 [mV]
range 1 :	100.6 [mV]
range 2 :	151.5 [mV]
range 3 :	200.2 [mV]

Other data is available on plots following this page.

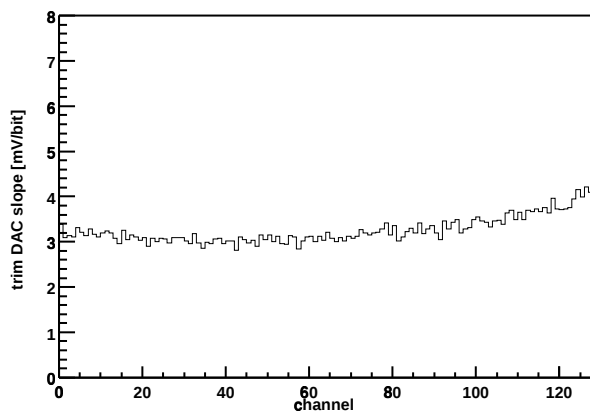
Chip Z36459A-W04-X10-Y18 Gain vs. channel



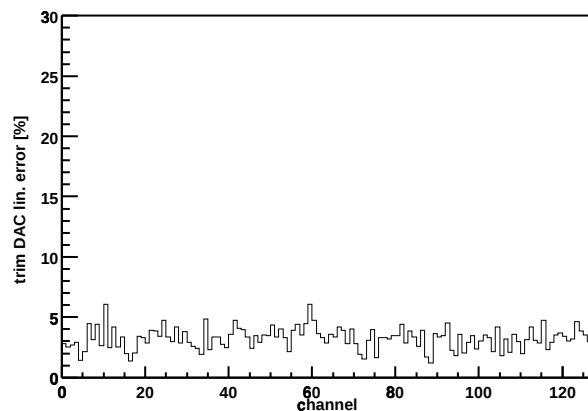
Chip Z36459A-W04-X10-Y18 Offset vs. channel



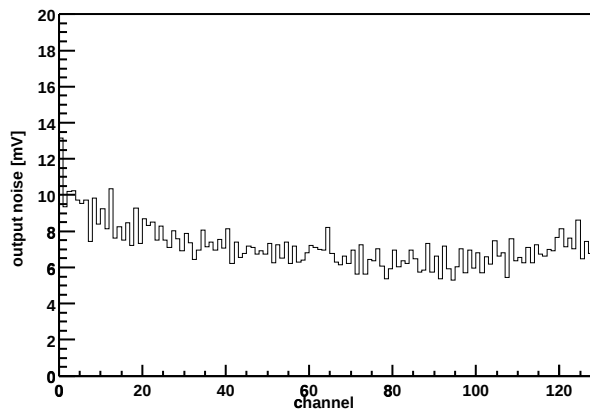
Chip Z36459A-W04-X10-Y18 Trim DAC slope vs. channel



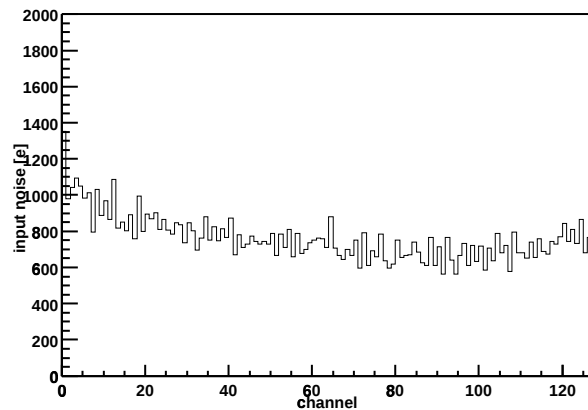
Chip Z36459A-W04-X10-Y18 trim DAC lin. error



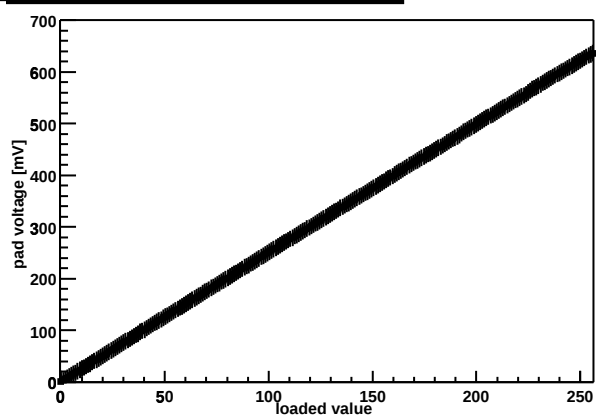
Chip Z36459A-W04-X10-Y18 Output noise vs. channel



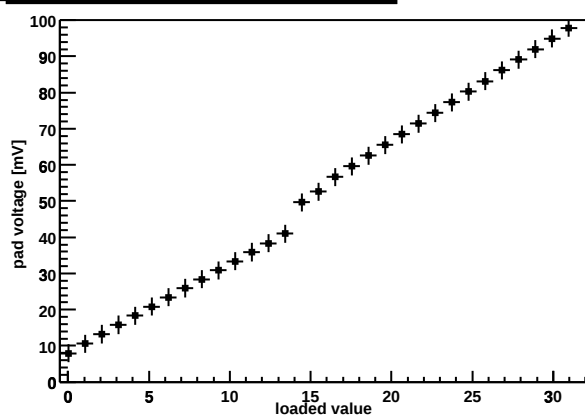
Chip Z36459A-W04-X10-Y18 Input noise vs. channel



Chip Z36459A-W04-X10-Y18 threshold DAC



Chip Z36459A-W04-X10-Y18 preamp DAC



Chip Z36459A-W04-X10-Y18 shaper DAC

