

Chip Z36459A-W04-X12-Y17 report.
(created: Thu Mar 29 20:54:40 2001)

General info :

Wafer name : Z36459A-W04
Position in wafer: X12-Y17
Raw data status flag : 63
Number of channels : 128
Digital and Analog volt. : 4.00 [V] 3.50 [V]
Master digital and analog supply current: 55.00 [mA] 80.00 [mA]
Slave digital and analog supply current : 35.00 [mA] 80.00 [mA]

BIN status: 0 (perfect chip)

Digital tests (minimum Vdd voltage [V] @40MHz and @50MHz) :

Config I/O test:	3.3	3.3
Address test:	3.3	3.3
L1 counter test:	3.3	3.3
Input reg. test:	3.3	3.3
Input lin. test:	3.3	3.3
Fake Slv. test:	3.3	3.3
Slave test:	3.3	3.4
Token test:	3.3	3.3

Digital perfect pattern: yes

Analog data:

Chip gain / sigma : 55.57 / 1.43 [mV/fC]
Chip offset / sigma : 17.70 / 7.08 [mV]
Chip output noise (max): 11.52 [mV]
Chip output noise (min): 5.19 [mV]
Quality factor: 7.70

DAC data:

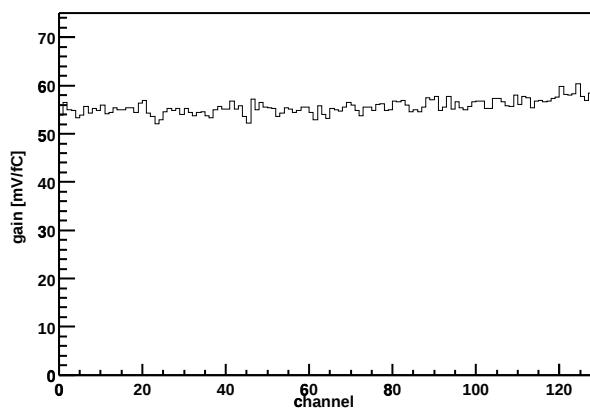
Threshold DAC slope:	2.53 [mV/bit]	lin. error:	0.3 [%]
Preamp. DAC slope:	3.12 [mV/bit]	lin. error:	5.2 [%]
Shaper DAC slope:	2.04 [mV/bit]	lin. error:	7.6 [%]

Range DAC data:

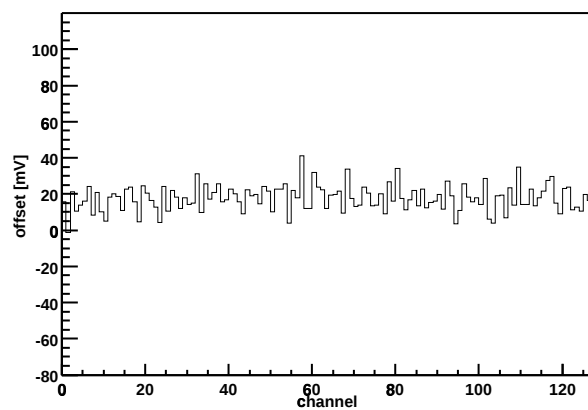
range 0 :	49.1 [mV]
range 1 :	98.4 [mV]
range 2 :	150.7 [mV]
range 3 :	199.4 [mV]

Other data is available on plots following this page.

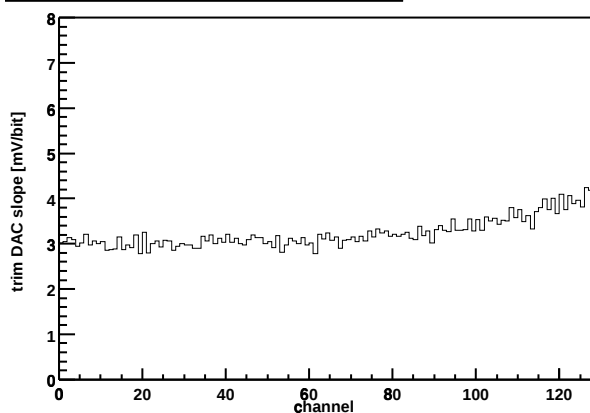
Chip Z36459A-W04-X12-Y17 Gain vs. channel



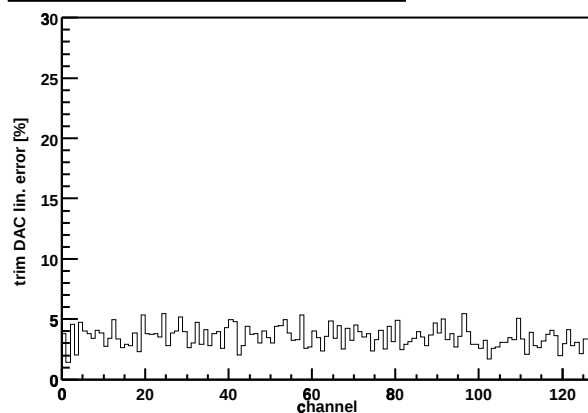
Chip Z36459A-W04-X12-Y17 Offset vs. channel



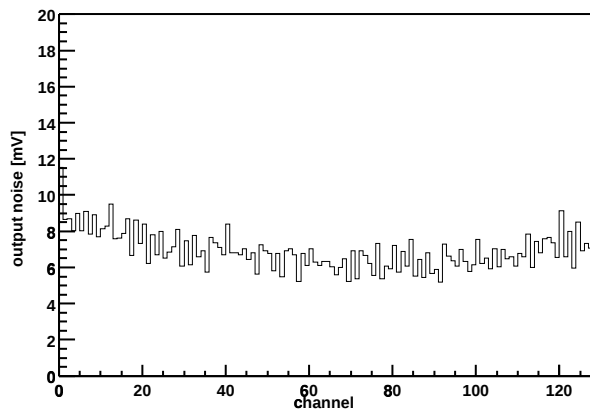
Chip Z36459A-W04-X12-Y17 Trim DAC slope vs. channel



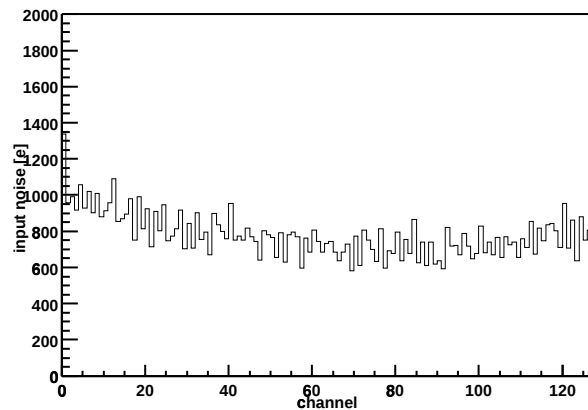
Chip Z36459A-W04-X12-Y17 trim DAC lin. error



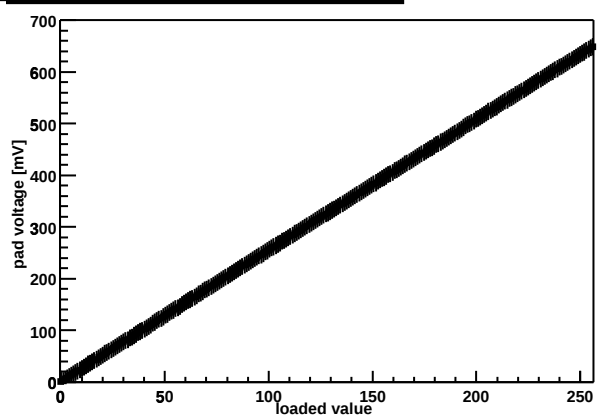
Chip Z36459A-W04-X12-Y17 Output noise vs. channel



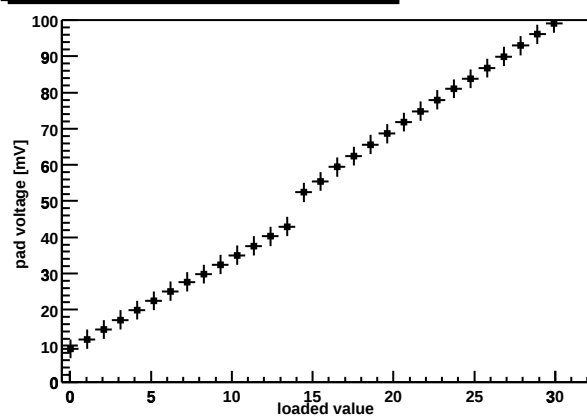
Chip Z36459A-W04-X12-Y17 Input noise vs. channel



Chip Z36459A-W04-X12-Y17 threshold DAC



Chip Z36459A-W04-X12-Y17 preamp DAC



Chip Z36459A-W04-X12-Y17 shaper DAC

