

ABCD Production Status

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SCT Week Dec-02**

As of end of Nov-2002:

Number of wafers received from ATMEL:	815
Number of wafers tested:	740
Percentage of perfect chips needed for SCT so far found:	74%
Yield of accepted wafers:	27.1%
Percentage of wafers rejected:	12.7%

We have also received 105 wafers so far in December with another 25 expected before the holiday break. The delivery schedule submitted by ATMEL will have all the release orders (minus some rejected wafers) fulfilled by mid-February with more replacement shipped in March.

The bad news is that the we have tested a few wafers from two lots fabricated since the fabrication re-start in June and their yield is very poor.

**13% for 10 wafers in one lot
< 5% for 5 wafers in a second lot**

ATMEL has been informed of the problem (last Friday) and they say they are “analyzing the information.”

There is a bottle neck in chip picking (i.e. selecting perfect chips from a sawed wafer and packaging them in gel-packs) at UCSC. We cannot satisfy the requested ship rate from KEK now that they have restarted hybrid assembly and are trying to make up for lost time.

We are pursuing a proposal from KEK for them to supplement the UCSC wafer sawing and chip picking with work in Japan.

First step is to qualify their vendor.