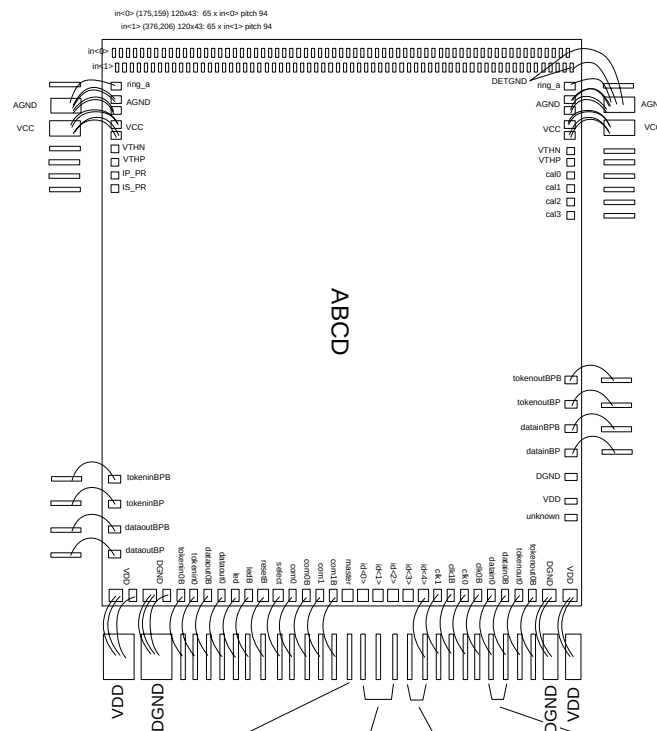


ISSUE
DATE 99/05/17

ABCD1: 6350(w) x 7500 (L)



Master
1st chip --- DGND
Others ----- Open

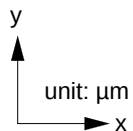
Last chip: extra bonds per hybrid side
datain0 ----- DGND
datain0B ---- VDD

id:	<0>	<1>	<2>
1st	G	G	G
2nd	--	G	G
3rd	G	--	G
4th	--	--	G
5th	G	G	--
6th	--	G	--

Hybrid side	
id: <3>	
Top	G
Bottom	--

id<4>	G
-------	---

G=DGND



INSTITUTE FOR PARTICLE AND NUCLEAR STUDIES
KEK HIGH ENERGY ACCELERATOR RESEARCH ORGANIZATION
OHO 1-1, TSUKUBA, IBARAKI 305-0801, JAPAN

ITEM NO.	TITLE	MATERIAL	QUANTITY	REMARKS
DESIGNED	SCALE 20/1	TITLE		
DRAWN Y. Unno	PAPER SIZE A4	ABCD1 chip loading		
CHECKED		DRAWING NO.		
		HY990517-1440A		